

## **Oxford Revise | OCR Computer Science | Answers**

**Chapter 34 Electronic logic** 

Question	Answer	Extra information	Marks	AO / Specification reference
1		1 mark for each correct logic gate, with the correct number of inputs and outputs, up to 2 marks.	1 1	AO1 2.4.1



Question	Answer			Extra information	Marks	AO / Specification reference
Question 2	Answer NOT A 0 1 AND A 0	<b>NOT A</b> 1 0 <b>B</b> 0	A AND B 0	Extra information1 mark for each correct truth table, up to 2 marks.Note: Each correct truth table must include the correct number of rows and columns. Other column headings may be used in this question, as none have been specified, but it must be obvious which are inputs and outputs. You are advised to list the rows in the	1	
	0	1	0	order shown, but other orders that include all possible combinations		
	1	0	0	are also acceptable.		



Question	Answer	Extra information	Marks	AO / Specification reference
3	<b>OR gate:</b> The output is TRUE if either or both of the inputs are TRUE, but FALSE if both inputs are FALSE.	1 mark for each correct statement, up to 2 marks.	1	A01
	<b>NOT gate:</b> The output is TRUE if the input is FALSE and FALSE if the input is TRUE.	Note: The answers given are examples. Other statements with the same meaning as these are also acceptable.	1	2.4.1